RPS920010125US1 C. E. Kuhlmann et al. Field Programmable Network Processor and Method for Customizing a Network Processor 1/3

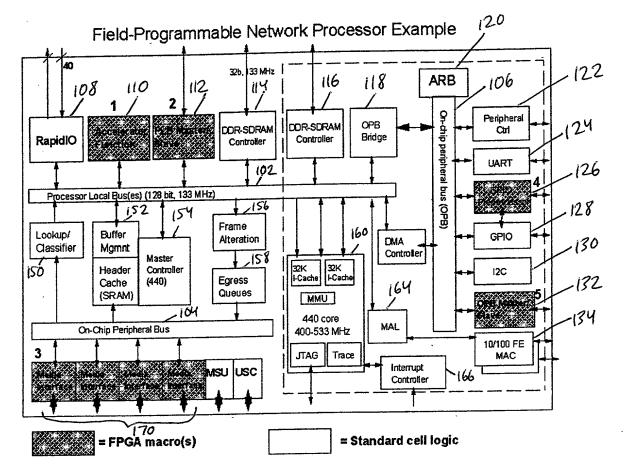


Figure 1 - FPGA blocks for User-specific functions and I/O interfaces

RPS920010125US1
C. E. Kuhlmann et al
Field Programmable Network Processor and
Method for Customizing a Network Processor
2/3

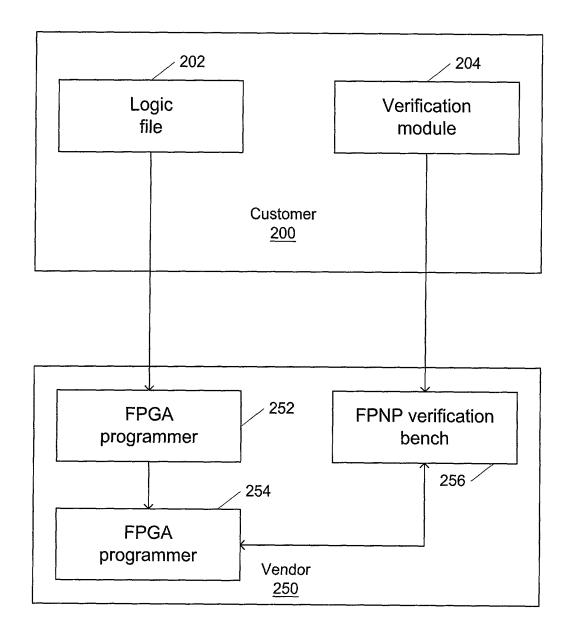


Fig. 2

RPS920010125US1 C. E. Kuhlmann et al. Field Programmable Network Processor and Method for Customizing a Network Processor 3/3

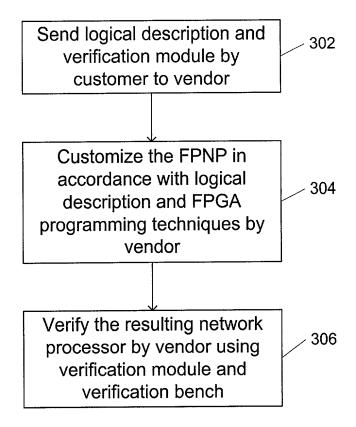


Fig. 3